

LM111 - Voltage Comparator



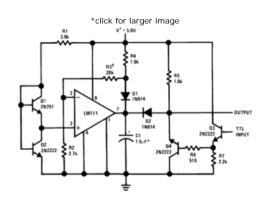
PowerWise



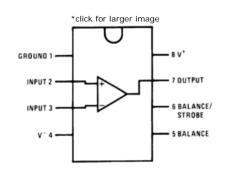
Features

- Operates from single 5V supply
- Input current: 150 nA max. over temperature
- Offset current: 20 nA max. over temperature
- Differential input voltage range: ±30V
- Power consumption: 135 mW at ±15V

Typical Application



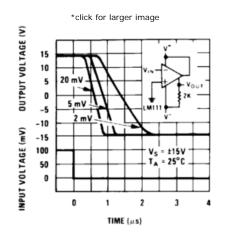
Connection Diagram



Parametric Table

Response Time	0.2, 0.1 us
Output Bus	Open Drain
Supply Min	5 Volt
Supply Max	36 Volt
Channels	1 Channels
Offset Voltage max, 25C	3 mV
Output Current	50 mA
Input Range	Not R-R
Supply Current Per Channel	5.1 mA
PowerWise Rating 3	1020, 510 uA x us
Max Input Bias Current	150 nA
Special Features	Offset Adjust, Strobe
Temperature Min	-55 deg C
Temperature Max	125 deg C
Function	Comparator
AEC Q-100 Automotive Grade	0

Typical Performance





Package Availability, Models

				Packa	ge		Factory Lead Time			Std	Package	
Part Number	Туре	Pins	Spec.	MSL Rating	Peak Reflow	RoHS Report	Weeks Qt	Models		Pack Size	Marking Format	
_M111J-8	CERDIP	8	STD	1	NA	RoHS	Full productio	n LM111.MOE		rail of	NSUZXYTT LM111J	
	OLIVDI	Ŭ		·		Kono	6 weeks 50	sks 500		40	-8	
_M111H	TO-99	8	STD	1	NA	RoHS	Full productio	n LM111.MOE		box of	NSZXYTTE# LM111H	
	10-33		NOPB	1	NA	Kono	6 weeks 200			500		
_M111 MD8		-	Un	packag	ed Die	<u> </u>	Full productio	n LM111.MOE		tray of	-	
				g			N/A 500	00		N/A wafer		
_M111 MW8	Wafer					Full productio	n LM111.MOE		jar	-		
			1				N/A 100	00		N/A		
5962-8687701Q2A (LM111E-SMD)	LCC	20				RoHS	Obsolete	LM111.MOE		rail of	NSZSSXXYYA Q 5962-	
				r –	r –		10 weeks 10 Obsolete	0		50 rail	8687701Q2A NSZSSXXYYA	
LM111E/883	LCC	20	STD	1	NA	RoHS	8 weeks 50	LM111.MOE		of 50	LM111E /883 Q	
5962-8687701QGA	TO-99	8				RoHS	Obsolete	LM111.MOD		tray of	NSZSSXXYYA Q	
(LM111H-SMD)	10-33				·	Kurio	8 weeks 10	0		20	5962-8687701QGA	
_M111H/883	TO-99	8	STD	1	NA	RoHS	Full productio	LM111.MOE		tray of	NSZSSXXYYA LM111H/883	
5962-8687701QPA		-				ļ	Obsolete			20 rail	NSZSSXXYYA	
(LM111J-8-SMD)	CERDIP	8				RoHS	8 weeks 200	LM111.MOE		of 40	Q 5962 8687701QPA	
LM111J-8/883	CERDIP	8	STD	1	NA	RoHS	Full productio	n LM111.MOE		rail of	NSZSSXXYYA LM111J-8	
							7 weeks 100	00		40 rail	/883 Q	
_M111J/883	CERDIP	14	STD	1	NA	RoHS	Full productio	LM111.MOE		of 25	NSZSSXXYYA LM111J/883 Q	
							Obsolete			rail	NS	
LM111W/883	CERPACK	10	STD	1	NA	RoHS	10 weeks 10	LM111.MOE		of 19	LM111W /883 Q	
		┝─				ļ					ZSSXXYYA NSZSS	
5962-8687701QZA (LM111WG-SMD)	CERPACK	10				RoHS	Obsolete	LM111.MOE		tray of	XXYYA 5962-	
							10 weeks 50	0		54	868770 1QZA Q	
							Full productio			tray	NS LM111WG	
_M111WG/883	CERPACK	10	STD	1	NA	RoHS	10 weeks 50	LM111.MOE 0		of 54	/883 Q ZSSXXYYA	
JM38510/10304BGA	TO 00						Full productio			tray	NS ZSSXXYYA 27014 Q	
(JL111BGA)	TO-99	8				RoHS	8 weeks 50	0 N/A		of 20	JM38510/10304BGA	
JM38510/10304BPA	CERDIP	8				RoHS	Obsolete	N/A		rail of	NS JM38510 /10304BPA	
(JL111BPA)	OLIVDI	0				Kurio	8 weeks 10			40	27014 Q ZSSXXYYA	
JM38510/10304BCA	CERDIP	14				RoHS	Obsolete	N/A		rail of	NS ZSSXXYYA JM38510/10304BCA	
JL111BCA)			-				6 weeks 50	0		25	27014 Q NS	
JM38510/10304BHA	CERPACK	10				Polle	Obsolete	N/A		rail	JM38510/ 10304BHA	
JL111BHA)	UERPAUK	10				RoHS	13 weeks 50			of 19	27014 Q	
							Obsolete			tray	ZSSXXYYA NS ZSSXXYYA	
JL111SGA	TO-99	8	STD	1	NA	RoHSo	N/A 10	N/A		of N/A	JM38510/10304SGA	

LM111HPQMLV	TO-99	8	STD	1	NA	RoHS		Obsol N/A	ete N/A	LM111.MOD			of N/A	LM111HPQV Q 5962P0052401VGA
5962L0052401VGA (LM111HLQMLV)	TO-99	8			1	RoHS		Full prod	uction 500	LM111.MOD			tray of 20	NSZSSXXYYA LM111HLQV Q 5962L0052401VGA
5962R0052402VGA (LM111HRLQMLV)	TO-99	8				RoHS		Full prod	uction 100	LM111.MOD			tray of 20	NSZSSXXYYA LM111HRLQV Q 5962R0052402VGA
JL111SPA	CERDIP	8	STD	1	NA	RoHS		Obsol		N/A			rail of	NSZSSXXYYA JM38510 /10304SPA
								N/A	N/A				N/A	27014 Q NSZSSXXYYA
5962L0052401VPA LM111J-8LQMLV)	CERDIP	8				RoHS		Full prod	100	LM111.MOD			rail of 40	LM111J-8L QV Q 5962L 0052401VPA
5962R0052402VPA (LM111J-8RLQMLV)	CERDIP	8				RoHS	D	Full prod	uction N/A	LM111.MOD			rail of 40	NSZSSXXYYA LM111J-8RL QV Q 5962R
								Obsol					rail	0052402VPA NS JM38510
JL111SHA	CERPACK	10	STD	1	NA	RoHS		N/A	50	N/A			of N/A	/10304SHA 27014 Q ZSSXXYYA
5962L0052401VHA								Full prod	uction				rail	NSLM111W LQV Q
(LM111WLQMLV)	CERPACK	10				RoHS		N/A	50	LM111.MOD			of 19	ZSSXXYYA 5962L005 2401VHA
5962R0052402VHA	CERPACK				RoHS		Full prod	uction	LM111.MOD			rail of	NSLM111W RLQV Q ZSSXXYYA	
(LM111WRLQMLV)					N/A	N/A	LINTTTINOD			19	5962R005 2402VHA			
JL111SZA	ISZA CERPACK 10 STD 1 NA		RoHS		Obsol	ete	N/A			tray of	NS JM38510 \10304SZA			
					N/A	N/A				N/A	27014 Q ZSSXXYYA			
_M111WGPQMLV	CERPACK	10	STD	1	NA	RoHS	⊣s	Obsol	ete	LM111.MOD			tray of	NSLM111W GPQV Q 5962P005
								N/A	N/A				N/A	2401VZA ZSSXXYYA
5962L0052401VZA	CERPACK	10				RoHS		Full prod	uction	LM111.MOD			tray of	NSLM111W GLQV Q 5962L005
(LM111WGLQMLV)								N/A	50				54	2401VZA ZSSXXYYA
5962R0052402VZA (LM111WGRLQMLV)	CERPACK	10				RoHS		Full prod	uction 100	LM111.MOD			tray of 54	NSLM111W GRLQV Q 5962R005 2402VZA
Obsolete Versio			- Dest	-		<u> </u>								ZSSXXYYA
Obsolete Part	JL111S		e Part o	n Sup	pilei	Nation	al Semico	Sourc	e		12/06/2005	e Buy Date		
_M111E-SMD	LM111V		83					IICONDUCT	OR CO	RP	12/03/2003			
_M111E-SMD	UA111	210						IICONDUCT			12/03/2008			
_M111E/883	LM111V	VG/8	83								12/03/2008			
_M111E/883	UA111	2,0					NATIONAL SEMICONDUCTOR CORP				12/03/2008			
_M111H-MLS	JL111S	GA									09/08/99			
_M111H-MLS	NONE					NATIONAL SEMICONDUCTOR				02/21/2006				
_M111H-SMD	UA111					NATIONAL SEMICONDUCTOR CORP				12/03/2008				
_M111HPQMLV	None					None				09/06/2005				
LM111J	NONE					NATIC	NATIONAL SEMICONDUCTOR			12/07/93				
LIVITIJ	LM111J	-8/88	33			NATIC	NAL SEN	IICONDUCT	OR CO	RP	12/03/2008			
LM111J-8-SMD		JL111SPA				NATIC	NAL SEM	11.			12/06/2005			
LM111J-8-SMD LM111J-8PQMLV	JL111S	JL111SHA			NSC			03/05/2008		1				
LM111J-8-SMD LM111J-8PQMLV LM111W-MLS	JL111S													
LM111J-8-SMD LM111J-8PQMLV	JL111S UA111					NATIC		IICONDUCT		RP	11/16/2008			
LM111J-8-SMD LM111J-8PQMLV LM111W-MLS LM111WG-SMD LM111WGPQMLV	JL111S UA111 A111					NATIC		IICONDUCT IICONDUCT		DRP	11/16/2008 09/06/2005			
LM111J-8-SMD LM111J-8PQMLV LM111W-MLS LM111WG-SMD	JL111S UA111					NATIC NATIC None	ONAL SEM		OR		11/16/2008			

General Description

The LM111, LM211 and LM311 are voltage comparators that have input currents nearly a thousand times lower than devices like the LM106 or LM710. They are also designed to operate over a wider range of supply voltages: from standard ±15V op amp supplies down to the single 5V supply used for IC logic. Their output is compatible with RTL, DTL and TTL as well as MOS circuits. Further, they can drive lamps or relays, switching voltages up to 50V at currents as high as 50 mA.

Both the inputs and the outputs of the LM111, LM211 or the LM311 can be isolated from system ground, and the output can drive loads referred to ground, the positive supply or the negative supply. Offset balancing and strobe capability are provided and outputs can be wire OR'ed. Although slower than the LM106 and LM710 (200 ns response time vs 40 ns) the devices are also much less prone to spurious oscillations. The LM111 has the same pin configuration as the LM106 and LM710.

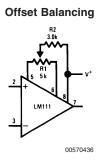


LM111/LM211/LM311 **Voltage Comparator 1.0 General Description**

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3.0 Typical Applications (Note 3)

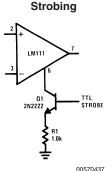




LM106 and LM710.

range of 0°C to +70°C.

- Differential input voltage range: ±30V
- Power consumption: 135 mW at ±15V



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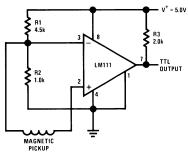
The LM211 is identical to the LM111, except that its perfor-

mance is specified over a -25°C to +85°C temperature range

instead of -55°C to +125°C. The LM311 has a temperature

Note: Do Not Ground Strobe Pin. Output is turned off when current is pulled from Strobe Pin.

Detector for Magnetic Transducer



00570439

Increasing Input Stage Current (Note 1)



Note 1: Increases typical common mode slew from 7.0V/µs to 18V/µs.

4.0 Absolute Maximum Ratings for the LM111/LM211(Note 10)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Total Supply Voltage (V ₈₄)	36V
Output to Negative Supply Voltage (V ₇₄)	50V
Ground to Negative Supply Voltage	
(V ₁₄)	30V
Differential Input Voltage	±30V
Input Voltage (Note 4)	±15V
Output Short Circuit Duration	10 sec
Operating Temperature Range	

LM111	–55°C to 125°C
LM211	–25°C to 85°C
Lead Temperature (Soldering, 10 sec)	260°C
Voltage at Strobe Pin	V+-5V
Soldering Information	
Dual-In-Line Package	
Soldering (10 seconds)	260°C
Small Outline Package	
Vapor Phase (60 seconds)	215°C
Infrared (15 seconds)	220°C
See AN-450 "Surface Mounting Methods	and Their Effect
on Product Reliability" for other methods	of soldering
surface mount devices.	

ESD Rating (Note 11)

Electrical Characteristics (Note 6) for the LM111 and LM211

Parameter	Conditions	Min	Тур	Max	Units
Input Offset Voltage (Note 7)	T _A =25°C, R _S ≤50k		0.7	3.0	mV
Input Offset Current	T _A =25°C		4.0	10	nA
Input Bias Current	T _A =25°C		60	100	nA
Voltage Gain	T _A =25°C	40	200		V/mV
Response Time (Note 8)	T _A =25°C		200		ns
Saturation Voltage	V _{IN} ≤–5 mV, I _{OUT} =50 mA		0.75	1.5	V
	T _A =25°C				
Strobe ON Current (Note 9)	T _A =25°C		2.0	5.0	mA
Output Leakage Current	V _{IN} ≥5 mV, V _{OUT} =35V		0.2	10	nA
	T _A =25°C, I _{STROBE} =3 mA				
Input Offset Voltage (Note 7)	R _S ≤50 k			4.0	mV
Input Offset Current (Note 7)				20	nA
Input Bias Current				150	nA
Input Voltage Range	V ⁺ =15V, V ⁻ =-15V, Pin 7	-14.5	13.8,-14.7	13.0	V
	Pull-Up May Go To 5V				
Saturation Voltage	V⁺≥4.5V, V [−] =0		0.23	0.4	V
	V _{IN} ≤–6 mV, I _{OUT} ≤8 mA				
Output Leakage Current	V _{IN} ≥5 mV, V _{OUT} =35V		0.1	0.5	μA
Positive Supply Current	T _A =25°C		5.1	6.0	mA
Negative Supply Current	T _A =25°C		4.1	5.0	mA

Note 4: This rating applies for ±15 supplies. The positive input voltage limit is 30V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30V below the positive supply, whichever is less.

Note 5: The maximum junction temperature of the LM111 is 150°C, while that of the LM211 is 110°C. For operating at elevated temperatures, devices in the H08 package must be derated based on a thermal resistance of 165°C/W, junction to ambient, or 20°C/W, junction to case. The thermal resistance of the dual-in-line package is 110°C/W, junction to ambient.

Note 6: These specifications apply for $V_S=\pm 15V$ and Ground pin at ground, and $-55^{\circ}C \leq T_A \leq +125^{\circ}C$, unless otherwise stated. With the LM211, however, all temperature specifications are limited to $-25^{\circ}C \leq T_A \leq +85^{\circ}C$. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single 5V supply up to $\pm 15V$ supplies.

Note 7: The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with a 1 mA load. Thus, these parameters define an error band and take into account the worst-case effects of voltage gain and R_S.

Note 8: The response time specified (see definitions) is for a 100 mV input step with 5 mV overdrive.

Note 9: This specification gives the range of current which must be drawn from the strobe pin to ensure the output is properly disabled. Do not short the strobe pin to ground; it should be current driven at 3 to 5 mA.

Note 10: Refer to RETS111X for the LM111H, LM111J and LM111J-8 military specifications.

Note 11: Human body model, 1.5 k Ω in series with 100 pF.

300V

5.0 Absolute Maximum Ratings for

the LM311 (Note 12)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Total Supply Voltage (V ₈₄)	36V
Output to Negative Supply Voltage	
(V ₇₄)	40V
Ground to Negative Supply Voltage	
(V ₁₄)	30V
Differential Input Voltage	±30V
Input Voltage (Note 13)	±15V
Power Dissipation (Note 14)	500 mW
ESD Rating (Note 19)	300V

Output Short Circuit Duration	10 sec
Operating Temperature Range	0° to 70°C
Storage Temperature Range	–65°C to 150°C
Lead Temperature (soldering, 10 sec)	260°C
Voltage at Strobe Pin	V+-5V
Soldering Information	
Dual-In-Line Package	
Soldering (10 seconds)	260°C
Small Outline Package	
Vapor Phase (60 seconds)	215°C
Infrared (15 seconds)	220°C
See AN-450 "Surface Mounting Methods	and Their Effect
on Product Reliability" for other methods	of soldering

surface mount devices.

Electrical Characteristics (Note 15) for the LM311

Parameter	Conditions	Min	Тур	Max	Units
Input Offset Voltage (Note 16)	T _A =25°C, R _S ≤50k		2.0	7.5	mV
Input Offset Current(Note 16)	T _A =25°C		6.0	50	nA
Input Bias Current	T _A =25°C		100	250	nA
Voltage Gain	T _A =25°C	40	200		V/mV
Response Time (Note 17)	T _A =25°C		200		ns
Saturation Voltage	$V_{IN} \leq -10 \text{ mV}, I_{OUT} = 50 \text{ mA}$ $T_A = 25^{\circ}\text{C}$		0.75	1.5	V
Strobe ON Current (Note 18)	T _A =25°C		2.0	5.0	mA
Output Leakage Current	V_{IN} ≥10 mV, V_{OUT} =35V T _A =25°C, I _{STROBE} =3 mA V ⁻ = Pin 1 = -5V		0.2	50	nA
Input Offset Voltage (Note 16)	R _s ≤50K			10	mV
Input Offset Current (Note 16)				70	nA
Input Bias Current				300	nA
Input Voltage Range		-14.5	13.8,-14.7	13.0	V
Saturation Voltage	V⁺≥4.5V, V [−] =0 V _{IN} ≤–10 mV, I _{OUT} ≤8 mA		0.23	0.4	V
Positive Supply Current	T _A =25°C		5.1	7.5	mA
Negative Supply Current	T _A =25°C		4.1	5.0	mA

Note 12: "Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits."

Note 13: This rating applies for ±15V supplies. The positive input voltage limit is 30V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30V below the positive supply, whichever is less.

Note 14: The maximum junction temperature of the LM311 is 110°C. For operating at elevated temperature, devices in the H08 package must be derated based on a thermal resistance of 165°C/W, junction to ambient, or 20°C/W, junction to case. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.

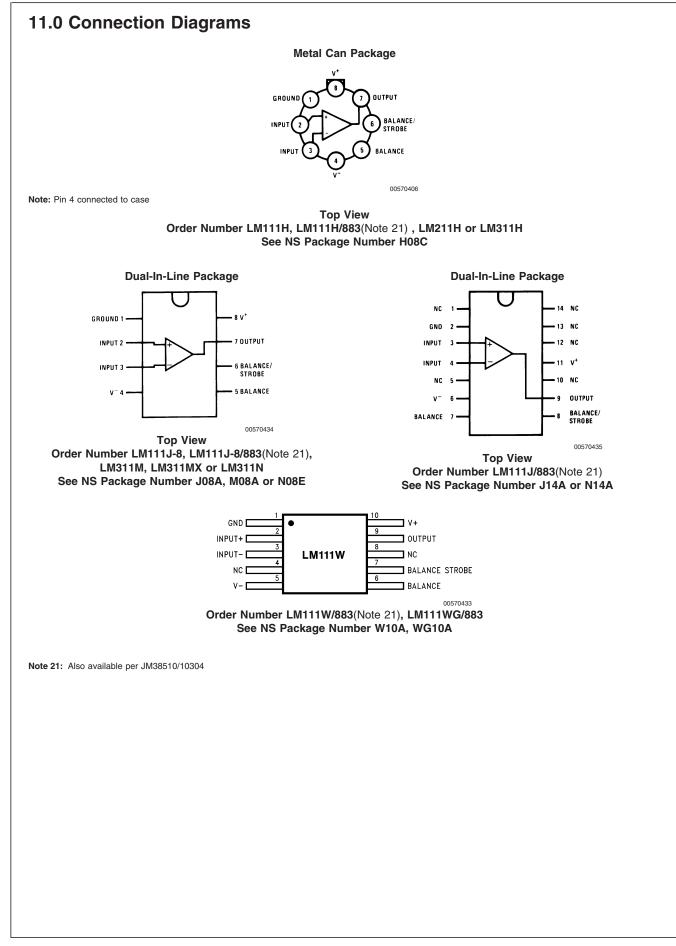
Note 15: These specifications apply for $V_S = \pm 15V$ and Pin 1 at ground, and $0^{\circ}C < T_A < +70^{\circ}C$, unless otherwise specified. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single 5V supply up to $\pm 15V$ supplies.

Note 16: The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with 1 mA load. Thus, these parameters define an error band and take into account the worst-case effects of voltage gain and R_S.

Note 17: The response time specified (see definitions) is for a 100 mV input step with 5 mV overdrive.

Note 18: This specification gives the range of current which must be drawn from the strobe pin to ensure the output is properly disabled. Do not short the strobe pin to ground; it should be current driven at 3 to 5 mA.

Note 19: Human body model, 1.5 k Ω in series with 100 pF.



LM111/LM211/LM311



